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| **Address:** |  M-132,Phase-1, Anna Nagar, Trichy-620026 Tamil Nadu (India)  |
| **Phone:** |  +91-9790277628 |
| **Email:** |  mr.karthik1132@gmail.com |
| **Birthdate:** |  08.03.1994 |
| **Nationality:** |  Indian |

 Karthik M R



Professional Experience

 12/2018 – till date

**Member Technical Staff at HCL Technologies LTD in Chennai (India)**

* Project : SPA CEM TDM-IP Development & Sustenance.
* Client: Cisco Systems Inc.
* Key activities:My core duty is to co-ordinate with senior associates to rectify the bugs & to automate Unit test using python.
* Hardware used:CISCO ASR 900 Series Router.
* Opearating System : Linux

 07/2017 – 12/2018

**Embedded Software Engineer at TRS FORMS & SERVICE PVT LTD in Chennai (India)**

* Project #1: Document Scanner
* Client:ICWA,Noida.
* Key activities:

 1)Developed complete firmare from scratch.

 2)Customized Driver codes for FTDI Chips.

 3)Interact with other software engineers to integrate firmware with application software.

* Hardware used:Silicon Labs C8051F320
* Opearating System : Windows
* Software used:Keil u Editor.
* Project #2: Fire Eye (Automatic Dummy numbering system ) / Fire Eye Lite
* Client:ICWA,Noida.
* Key activities:

 1)Study of entier firmware code with respect to printing process.

 2)Customization & Implementation of new fonts in printing logic.

 3)Bug fixing & Performance improment in terms of data loss.

 4) For Fire Eye lite version modified the sensor logic & interfaced the stepper motor

* Hardware used:Silicon Labs C8051F340,Cypress CY7C68013A
* Opearating System : Windows
* Software used:Keil u Editor.

Education

2016 – 2017

**PG Diploma in Embedded System Design, C-DAC – Noida**

* Specialization: Embedded System Design.
* Percentage: 64.57%.

2012 – 2016

**Bachelors of Engineering, Anna University Chennai**

* Specialization: Electronics & Communication Engineering.
* CGPA: 6.36/10

Software Skills

* Embedded C, C/C++,Python(Experienced)
* RS-232, SPI, I2C, CAN, Microcontroller Programming.
* Knowledge of oscilloscope, Knowledge of logic analysers.
* Real Time Operating System (RTOS), Knowledge of L2/L3 Protocols.
* Linux internals (POSIX Threads, IPC, Semaphores, Shared memory & Message queues).
* Have hands on experience in version control Git.

Languages Known

* Tamil
* English
* Hindi

Skills and training

* Python (HCL Internal Training)
* Basic Networking ( HCL Internal Training).
* Enrolled myself in Product management course in Udemy.

I hereby declare that the above mentioned information is accurate

Location: Chennai Karthik M R

Date: 15th March 2020