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| **Address:** | Hafenstraße 4, 38442 Wolfsburg |
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| **Email:** | vigneshvicky1123@gmail.com |
| **Birthdate:** | 05.06.1995 |
| **Nationality:** | Indian |

 Vignesh Muthusamy



Professional Experience

03/2018 – 01/2020

**PCB Layout engineer in Siart design systems in Banglore**

* Key activities
* Developing PCB layouts for high speed/reliable circuits and resolving design related issues in timely manner
* Performing high speed interface routing like DDR4, PCIE, USB2.0/USB3.0, and connectors
* Handling RF board routing as per RF routing guidelines
* Managing the design, fabrication, and procurement processes to acquire the PCB’s in a timely and cost efficient manner
* Well versed with routing techniques, reducing Cross talk, DFM, DFA, DUT board and BOM and knowledge of IEEE and IPC standards

 07/2016 – 02/2018

**PCB Layout engineer in Cube circuits pvt Ltd in Coimbatore**

* Key activities
* Developing PCB layouts for ATE board and resolving design related issues in timely manner
* Collaborating with project team for circuit requirements and limitations to develop design documentation and requirements specifications for assigned projects
* Managing the activities of Placement, Routing, Length Matching, and Gerber Generation
* Adroit at schematic creation, layout library management, components creation, placement and routing, debugging errors, and Gerber generation

Education

2012 – 2016

**Bachelors in Electronics and communication Engineering, Anna university Coimbatore**

* CGPA:6.4

Software Skills

* Cadence Allegro (Experienced)
* Orcad Capture (Experienced)
* Altium (Basics)
* Cam350 (Experienced)
* AutoCAD (Basics)
* Cadence Allegro package designer (Basics)

Languages Known

* Deutsch – B1
* English – C1
* Tamil- Mother tongue

Skills and training

* PCB Design & Analysis
* PCB Layout & Verification
* High Speed Deigns
* ATE & Commercial Board Designs
* Placement & Length Matching
* Gerber Generation
* Troubleshooting
* Quality & Safety Management
* Evaluating Requirements
* Board Routing & Guidelines
* Customer Service Management
* Debugging Errors
* Procurement Process
* Cost Control & Estimation
* Team Management
* Project Management
* Performance Management
* Documentation

Technologies known

* Memory (DDR4, LPDDR, QDR, eMMC)
* Chipsets (Intel, ARM, DSP)
* Interfaces ( SERDES, PCIE, SATA, USB 3.0)
* HDMI
* Analog High speed ADC/DAC
* High sensitivity circuits
* RF boards (Modems, Communications)
* Power boards (Switching circuits, High voltage, High current)